

100mA / 50V Digital transistors

(with built-in resistors)

DTA114EB / DTA114EM / DTA114EE / DTA114EUA / DTA114EKA

Applications

Inverter, Interface, Driver

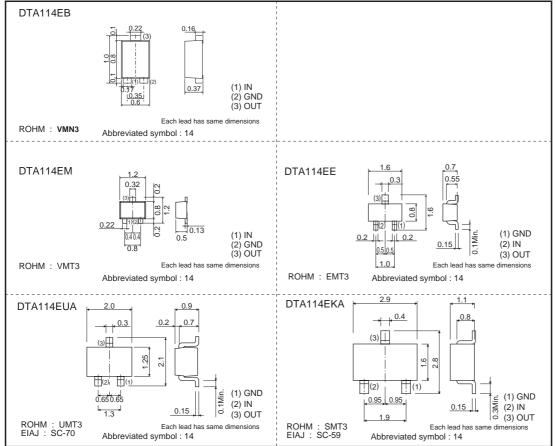
Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making the device design easy.

Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

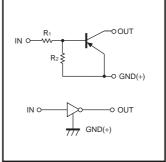
•Dimensions (Unit : mm)



Packaging specifications

	Package	VMN3	VMT3	EMT3	UMT3	SMT3
	Packaging type	Taping	Taping	Taping	Taping	Taping
	Code	T2L	T2L	TL	T106	T146
Туре	Basic ordering unit (pieces)	8000	8000	3000	3000	3000
DTA114EB		0	-	-	-	_
DTA114EM		-	0	-	-	-
DTA114EE		-	-	0	-	-
DTA114EU	ł	-	-	-	0	-
DTA114EKA	A	-	-	-	-	0

•Equivalent circuit



R1=R2=10kΩ

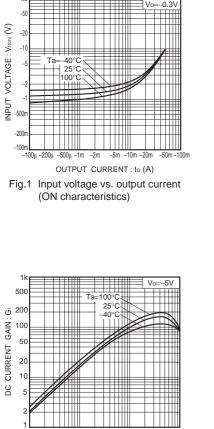
•Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits						
Falameter		DTA114EB	DTA114EM	DTA114EE	DTA114EUA	DTA114EKA	- Unit	
Supply voltage	Vcc	-50					V	
Input voltage	Itage VIN		-40 to +10					
Output compart	lo	-50						
Output current	IC(Max.)	-100					mA	
Power dissipation	Pd	150 200		mW				
Junction temperature	Tj	150			°C			
Storage temperature Tstg -55 to -		–55 to +150)		°C			

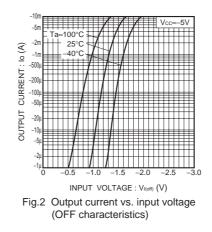
•Electrical characteristics (Ta=25°C)

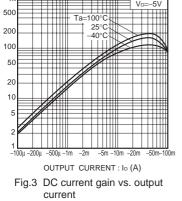
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Innut voltoge	VI(off)	-	-	-0.5	N	Vcc=–5V, Io=–100μA	
Input voltage	VI(on)	-3	-	-	V	Vo=-0.3V, Io=-10mA	
Output voltage	VO(on)	-	-	-0.3	V	lo/l=-10mA/-0.5mA	
Input current	h	-	-	-0.88	mA	VI=-5V	
Output current	IO(off)	-	-	-0.5	μΑ	Vcc=-50V, VI=0V	
DC current gain	Gı	30	-	-	-	Vo=–5V, Io=–5mA	
Input resistance	R1	7	10	13	kΩ	-	
Resistance ratio	R2/R1	0.8	1	1.2	-	-	
Transition frequency	f⊤*	_	250	_	MHz	Vce=-10V, Ie=5mA, f=100MHz	
* Characteristics of built in transistor							

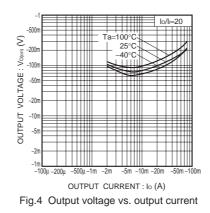
* Characteristics of built-in transistor



•Electrical characteristic curves







	Notes
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